

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Joseph Francis Mann, et al.  
Serial No.: 10/083,814  
Filed: February 27, 2002  
For: Processor with Versatile External Memory Interface  
Art Unit: 2116  
Examiner: Tse W. Chen  
Docket No.: 1506.010

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**RESPONSE AFTER FINAL**

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**MAIL STOP AF  
COMMISSIONER FOR PATENTS  
PO BOX 1450  
ALEXANDRIA VA 22313-1450**

Dear Sir:

In response to the Office Action of November 7, 2006, please consider the following remarks:

**Amendments to the Claims begin on page 2 of this communication.**

**Remarks in support of patentability begin on page 7 of this communication.**

AMENDMENT  
NOT  
TO BE  
ENTERED  
for  
1/16/07